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Date: August 25, 2004

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From: Daniel M. Cavanagh

Reg No. 41,661

Re: Application No. 10/721,910; Filed November 24, 2003

Entitled METHOD OF OVERTONE SELECTION AND LEVEL CONTROL

IN AN INTEGRATED CIRCUIT CMOS NEGATIVE RESISTANCE

OSCILLATOR TO ACHIEVE LOW JITTER

File: 51592/DMC/V165

I HEREBY CERTIFY THAT THIS PAPER IS BEING FACSIMILE TRANSMITTED TO THE UNITED STATES PATENT AND TRADEMARK OFFICE ON August 25, 2004.

Renne M.F. Wyzykowski

*Correspondence: Petition to Correct Filing Receipt and Filing Date

Copy of Postcard (Exhibit A)

Copy of Express Mail Receipt (Exhibit B)
Copy of First Page of Application (Exhibit C)

Copy of Transmittal Letter (Exhibit D)

Copy of Filing Receipt

1079763.1

Christie, Parker & Hale, LLP

350 West Colorado Boulevard Post Office Box 7068 Pasadena, CA 91109-7068

626-795-9900

Fax: 626-577-8800

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Renne M.F. Wyzykowski

Applicant

: Raymond B. Patterson, et al.

Confirmation No. 6287

Application No. : 10/721,910

Filed

: November 24, 2003

Title

: METHOD OF OVERTONE SELECTION AND LEVEL CONTROL IN

AN INTEGRATED CIRCUIT CMOS NEGATIVE RESISTANCE

OSCILLATOR TO ACHIEVE LOW JITTER

Grp./Div.

: 2816

Examiner Docket No. : Not Assigned : 51592/DMC/V165

Customer No.

: 23363

PETITION TO CORRECT FILING RECEIPT AND FILING DATE

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Post Office Box 7068 Pasadena, CA 91109-7068 August 25, 2004

Applicant has now become aware that the filing receipt for the application indicates an incorrect filing date and respectfully petitions to correct the filing date for the above-identified application. This application was filed by Express Mail on November 24, 2003. As evidence of the correct filing date, Applicant submits herewith a copy of the postcard (Exhibit A), a copy of the Express Mail Receipt (which has a Date In date of November 24, 2003) (Exhibit B), a copy of the first page of the application indicating Express Mail No. EV 327292141 US (Exhibit C), and a copy of the transmittal letter (Exhibit D).

Applicant Petitions to have the filing date corrected to be November 24, 2003, and requests that applicant be provided a corrected filing receipt indicating same. The Commissioner is hereby authorized to charge Deposit Account No. 03-1728 for any fees which may be required in this connection.

Application No. 10/721,910

A Corrected Filing Receipt indicating that this application has been assigned the filing date of November 24, 2003 is respectfully requested.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

Daniel M. Cavanagh Reg. No. 41,661 626/795-9900

DMC/rmw

Enclosure: Acknowledgement Postcard

Express Mail Receipt First Page of Application

Transmittal Letter

Filing Receipt
RMW IRV1079740.1-*-08/25/04 9:01 AM

FEE TRANSMITTAL UTILITY PATENT APPLICATION

DATE: November 24, 2003

Docket No.

51592/DMC/V165

Inventor(s)

Raymond B. Patterson III; Randy T. Heilman

Title

: METHOD OF OVERTONE SELECTION AND LEVEL

CONTROL IN AN INTEGRATED CIRCUIT CMOS

NEGATIVE RESISTANCE OSCILLATOR TO ACHIEVE

LOW JITTER

Duplicate ___

FEE DETERMINATION

	C	LAIMS AS F	TLED		· · · · ·
	NUMBER FILED	NUMBER EXTRA	SMALL ENTITY RATE	LARGE ENTITY RATE	FEE
TOTAL CLAIMS	20-20	= 0	x \$9.00	0 x \$18.00	-0-
INDEPENDENT CLAIMS	3 3	= 0	x \$43.00	0 x \$86.00	-0-
MULTIPLE-DEPENDE	NT CLAIMS FEI	3	\$145.00	\$290.00	-0-
BASIC FEE			\$385.00	\$770.00	\$770
		,	TOT	AL FILING FEE	\$770
List Independent Claims	: 1, 12, 17				

METHOD OF PAYMENT

X No filing fee enclosed.

X No Deposit Account Authorization.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

Зу <u>_____</u>

Daniel M. Cavanagh Reg. No. 41,661 626/795-9900

DMC/rmw

RMW IRV1072081.1-*-11/24/03 2:57 PM

Page 1 of 2



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS	
10/721,910	11/26/2003	2816	0.0 ₹ 🗷	CE 13/15920MC/V165	8	20	3	

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CONFIRMATION NO. 6287

CHRISTIE, PARKER & HALE, LLP P.O. BOX 7068 PASADENA, CA 91109-7068

Christie, Parker & Hale, LLP

FILING RECEIPT

OC000000011984659

Date Mailed: 02/27/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Raymond B. Patterson III, Melbourne, FL; Randy T. Heilman, Palm Bay, FL;

51592	AGTION
REMINDER	BUE DATE
DEABLINE	

1/1/4に

Domestic Priority data as claimed by applicant

This application is a CON of 10/346,557 01/16/2003 ABN which is a CON of 10/163,860 06/05/2002 ABN which claims benefit of 60/296,321 06/05/2001

Foreign Applications

If Required, Foreign Filing License Granted: 02/26/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Method of overtone selection and level control in an integrated circuit CMOS negative resistance oscillator to achieve low jitter

Page 2 of 2

Preliminary Class

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Parent Case ____

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	PATENT:ORD PRO PCT;ORD	Extension of Time in Parent Case 49577	OTHER Copy of Petition for	Petition Extension of Time	Info Disc Stint/PTO-1449	Small Entity Declaration	Informal/Formal Drawings	Declaration & Pwr of Atty	Application (No. of Pages)	dransmittal Letter(s)	Check(s) HONE	IN THE U.S. PATENT AND TRADEMARK OFFICE Title: Method of Overtone Selection and Leve Control in an Integrated Circuit CMO Negative Resistance Oscillator to Achieve Priority Claimed? — YES Assignment Enclosed (List Assignee)
COLLCERT;	ORDPROPCT;ORP CPP DIVXXX CON CIPREIREX OF	Parent Case 49577	for	ent. () pas.)	N/A	N/A	o sneets	COPY	27 pages	Patent Mark 3 pgs in dup.		EU.S. PATENT AND TRADEMARK OFFICE Method of Overtone Selection and Level Control in an Integrated Circuit CMOS Live Resistance Oscillator to Achieve Claimed? YES Signment Enclosed (List Assignee) Experiment Enclosed (List Assignee)
ITU44d44eDIV	1 REX OF DOCKET 4 7 5 7 7				10/721910	22388 11 6 570		SERIAL NUMBER/FILING DATE	PLEASE RETURN THIS POSTCARE	Checked by:		Client ID NEW V165 OS Case No 51592 e. Atty/Sec DMC: rns Date Mailed Nov. 24, 2003 Express Mail No. EV32729214108

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45427/JFO/V165

METHOD OF OVERTONE SELECTION AND LEVEL CONTROL IN AN INTEGRATED CIRCUIT CMOS NEGATIVE RESISTANCE OSCILLATOR TO ACHIEVE LOW JITTER

5 CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims the benefit of U.S. Provisional Application, No. 60/296,321, filed June 5, 2001, the disclosure of which is incorporated by reference.

BACKGROUND

The present invention generally relates to generating clock pulses and in particular to generating a stable high-speed clock pulse train by employing a negative resistance oscillator operating at an overtone frequency of a resonator.

Applications such as communications, video processing, data 15 acquisition and recovery, and high-bandwidth test systems require stable high frequency clocks meet ţo specific performance demands and provide periodic and accurately spaced Most clocks include an electronic device, e.g., oscillator, to generate an accurate oscillating signal. signal oscillation is based on a periodic event that, under the 20 control of a resonator, repeats at a natural and substantially constant rate, i.e., a fundamental or resonance frequency.

stability is an inherent property that determines how well an oscillator can produce the same frequency over a given time interval. The more stable the oscillator, the more accurately the fundamental frequency is produced, and the less variations there will be in signal characteristics. Additionally, quality factor, or Q, is another inherent oscillator quality that influences stability. In general, oscillators having a high quality factor tend to be more stable, experience fewer signal

Express Mail No. EV327292141US

	(C		Y PATENT APPLICATION To nonprovisional applications u					
Docket I Inventor Title		: : : ! No. :	51592/DMC/V165 Raymond B. Patterson III; Randy T. Heilman METHOD OF OVERTONE SELECTION AND LEVEL CONTROL IN AN INTEGRATED CIRCUIT CMOS NEGATIVE RESISTANCE OSCILLATOR TO ACHIEVE LOW JITTER EV 327292141 US					
ADDRE	SS TO:		Patent Application ioner for Patents 1450					
			ia, VA 22313-1450	November 24, 2003				
<u>x</u>	2003 Prior This	which is a capplication application	continuation of patent application information: Examiner Not Ass	onal Application No. 60/296,321, filed				
3. APP	LICATION	OMPRI	SED OF					
Speci	ification Speci	fication, cla	ims and Abstract (total pages)					
Draw 8		s of formal	drawing(s) (FIGS. 1 to 8)					
Decla X	Newl Unex Copy	Power of A y executed ecuted deck from a prio EXECUTED	aration r application (37 CFR 1.63(d))(f	or continuation and divisional)				
4	Micr	ofiche Com	puter Program (Appendix)					

Paper Copy (identical to computer copy) Statement verifying identity of above copies

Computer Readable Copy

Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)

Do	UTILITY PATENT APPLICATION TRANSMITTAL (Only for new nonprovisional applications under 37 CFR 1.53(b)) Docket No.: 51592/DMC/V165					
6.	APPLIC	ANT(S) STATUS UNDER 37 CFR §1.27 Applicant(s) and any others associated with it/them under §1.27(a) are a SMALI ENTITY				
7.	ALSO E	NCLOSED ARE				
٠-		Request and Certificate under 35 U.S.C. §122(b)(2)(B)(i) Request for Non-Publication)				
	X	Preliminary Amendment				
		X Includes "Cross-Reference to Related Applications"				
	X	A copy of the Petition for Extension of Time for the parent application is enclosed				
		An Assignment of the invention with the Recordation Cover Sheet and the recordation				
		fee are enclosed				
	· ·	This application is owned by pursuant to an Assignment recorded at Reel, Frame				
		Information Disclosure Statement (IDS)/PTO/SB/08A/B				
		Copies of IDS Citations				
		Certified copy of Priority Document(s) (if foreign priority is claimed)				
		English Translation Document (if applicable) Return Receipt Postcard (MPEP 503) (should be specifically itemized).				
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8. CORRESPONDENCE ADDRESS

Other:

CHRISTIE, PARKER & HALE, LLP, P.O. BOX 7068, PASADENA, CA 91109-7068 Customer Number: 23363

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

Daniel M. Cavanagh Reg. No. 41,661 626/795-9900

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